March 14, 2011 – Grenoble, France
10:00-18:00
www.ecsi.org/s4d

Workshop Chair: Markus Winterholer, Cadence Design Systems

Workshop Description
Embedded systems and software complexity is rapidly increasing; from one processor to ten or more, from thousands of lines of code to millions. Embedded systems debug capabilities must scale with this exponential growth in system complexity. Without your research and innovation in embedded system debug, the time to discover and root-cause issues in increasingly complex system will grow as well. This extra time will delay product shipments and threaten to decrease the overall quality of the system when it is released. In today’s competitive marketplace, fortune or failure is often determined by time-to-market and quality. Effective embedded debug and debug automation is a cornerstone of success. Come present research, tools, or methodologies that help eliminate bugs embedded in hardware and/or software at any stage of product development. Potential topics include market/human research on trends in debug requirements for developers of embedded systems and standardization activities for debug and debug automation.

This workshop brings together users, solution provider and academia with an interest in debugging of embedded systems. The topics cover the full range of system debugging from specification to application with a special focus on multi-core and debug automation. Presentations of interest at this event are covering needs and requirements for innovations in debugging, methodologies and standards to make embedded hardware and software more reliable and easy to debug.

Call for Presentations
Submission Deadline: February 14, 2011

Topics of Interest
• System Level Debug
• System Performance Analysis and Optimization
• Embedded Software Debug
• ESL Model-based System Debug
• FPGA based System Debug
• Silicon Debug
• Design and Synthesis for Debug
• DFT Reuse for Debug and Analysis
• Debugging of IP blocks and their Integration
• Debug Architectures and Interfaces
• Digital/Analog Debug
• Tools for Debug, Analysis and Optimization
• Tools and Equipment Impact
• Debug Case Studies
• Debug R&D Initiatives
• Debug Standards

This list is not exclusive, please propose other topics if appropriate.

Presentation & Demonstration Proposal Submission
Submissions & Acceptance
The presentation & demonstration proposals should contain the title, presenter, affiliation and a short abstract of 10-20 lines. The workshop will offer demonstration sessions at which tools, solutions, experiments and best practices can be presented. Demonstration facilities will be provided uniquely to ECSI members and workshop sponsors (please read conditions on our website at www.ecsi.org/s4d). Proposals should be sent by February 14, 2011 to s4d2011-workshop@ecsi.org. Final acceptance of presentations and demos as well as agenda publication is expected on February 21, 2011.

Publication
All presentations will be available in PDF format in the ECSI Resource Center at www.ecsi.org.

Registration
The workshop is open to all interested parties. Registration is possible at www.ecsi.org/s4d. ECSI members can benefit from one free registration and additional registrations at a 50% discount.

Location
World Trade Center, Centre de Congrès et Seminaire, room: Makalu, 7 Place Robert Schuman, 38000 Grenoble, France

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